

**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

Application No.: 09/682,131  
Applicant: Fujio et al.  
Filing Date: July 25, 2001  
Title: **SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE WITH EMI  
PREVENTION STRUCTURE**

Atty. Docket: JP9-2000-0229  
Today's Date: March 3, 2004  
Examiner: Chris C. Chu  
Group Art Unit: 2815  
Fax: 703-872-9306

**RECEIVED  
CENTRAL FAX CENTER**

MAR 03 2004

**Amendments Under 37 CFR 1.114****Amendment E****OFFICIAL**

Honorable Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

In response to the Office Action dated December 9, 2003, Applicants respectfully request reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.

| <b>CERTIFICATE OF MAILING</b>  |  |
|--|--|
| I hereby certify that, on the date shown below, this correspondence is being:  |  |
| <b>MAIL</b>  | <b>FACSIMILE</b>   |
| <input type="checkbox"/> deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450. | <input checked="" type="checkbox"/> transmitted by facsimile to the Patent and Trademark Office. |
| Date: <u>3/3/2004</u>  | <u>Arnette Dodge</u><br>Name   |
|  | <u>ARNETTE DODGE</u><br>Signature  |

JP9-2000-0229  
09/682,131

Page 1 of 7

\*\*\*\*\* FACSIMILE COVER SHEET \*\*\*\*\*

MAR 03 2004 14:32

Message To:

917038729306

Message From:

BTU IPLAW

07

Pages

Follow This Cover Page